

Abstract of the Disclosure

A method for apparatus for masking a postamble ringing phenomenon in a DDR SDRAM comprising the steps of storing data, which are applied from a memory controller, in a data input latch through a data buffer and aligning the stored data, controlling the data input latch in such a manner that the data stored in the data input latch do not change, transmitting the data stored in the data input latch to a data input/output detection amplifier, enabling the data input latch to receive new data after the data, which have been transmitted to the data input/output detection amplifier, are transmitted to a global input/output line. In the method, a stable write operation can be performed even in devices having high operation speeds, such data rates above 400MHz. Thus, a stable write operation can be performed under conditions wherein the t_{DQSS} has a value of $0.75t_{CK}$ to $1.25t_{CK}$.